



LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING (Autonomous)

L.B.REDDY NAGAR, MYLAVARAM-521 230.A.P. INDIA

Affiliated to JNTUK, Kakinada & Approved by AICTE, New Delhi

NAAC Accredited with "A" grade, Accredited by NBA

New Delhi & Certified by ISO 9001:2008

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

<http://www.lbrce.ac.in>, ecedept4@gmail.com Phone: 08659-222933, Fax: 08659-222931

Department of ECE conducted One Week Faculty Development Program on "Recent Trends in VLSI and Embedded Systems" from 26th September - 1st October 2016.

Resource Persons

1. Mr. Ramesh Naidu, Technical Manager, Xilinx, Hyd
2. Mr. Sarath Kanth, Sr. Application Engg, Xilinx, Hyd
3. Mr. Prakash G, Sr. Application Engg, Xilinx, Hyd
4. Mr. Bala Chandar, Sr. Application Engg, Xilinx, Hyd

Major Topics addressed:

1. Trends in VLSI Design and Technology
2. ASIC Design Issues and Mentor Graphics Tools
3. Xilinx Vivado Design Suite Tool Flow
4. High Level Synthesis with the Vivado HLx
5. Zynq All Programmable SoC
6. Hands on sessions

Total No. of Participants-77

One week faculty development program on Recent Trends in VLSI and Embedded Systems has been conducted from 26th Sept to 1st Oct 2016 and all participants are trained on VLSI Design and ASIC design issues using Mentor graphics tools and Xilinx tools through internal and external resource persons. All participants are actively involved in hand on sessions to get practical exposure and insight into the core design issues.

